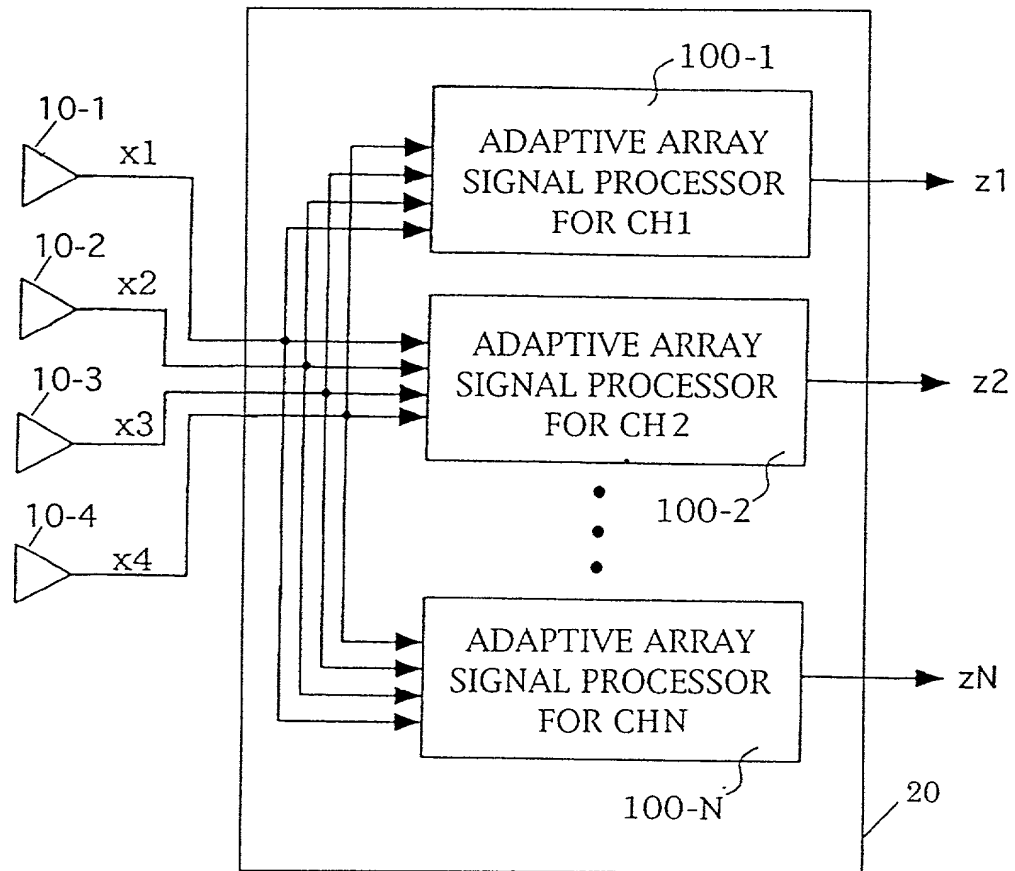


FIG. 1
(PRIOR ART)

1



ANTENNA SIGNAL PROCESSING SYSTEM

FIG. 2
(PRIOR ART)

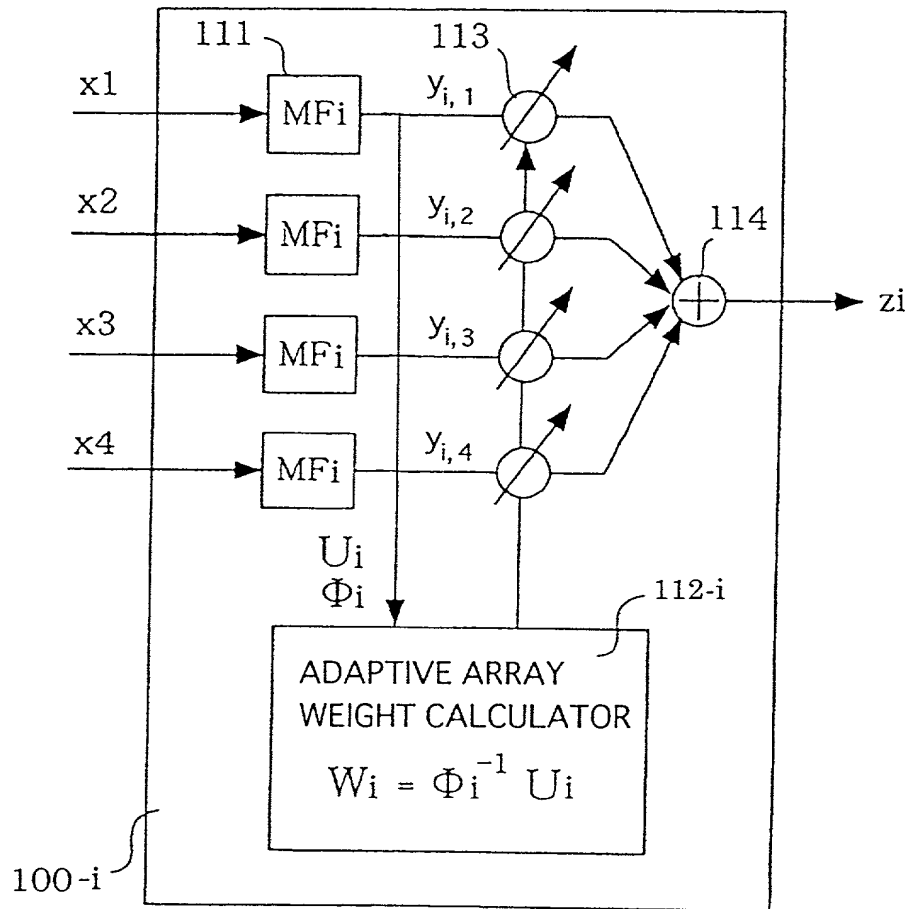


FIG. 3

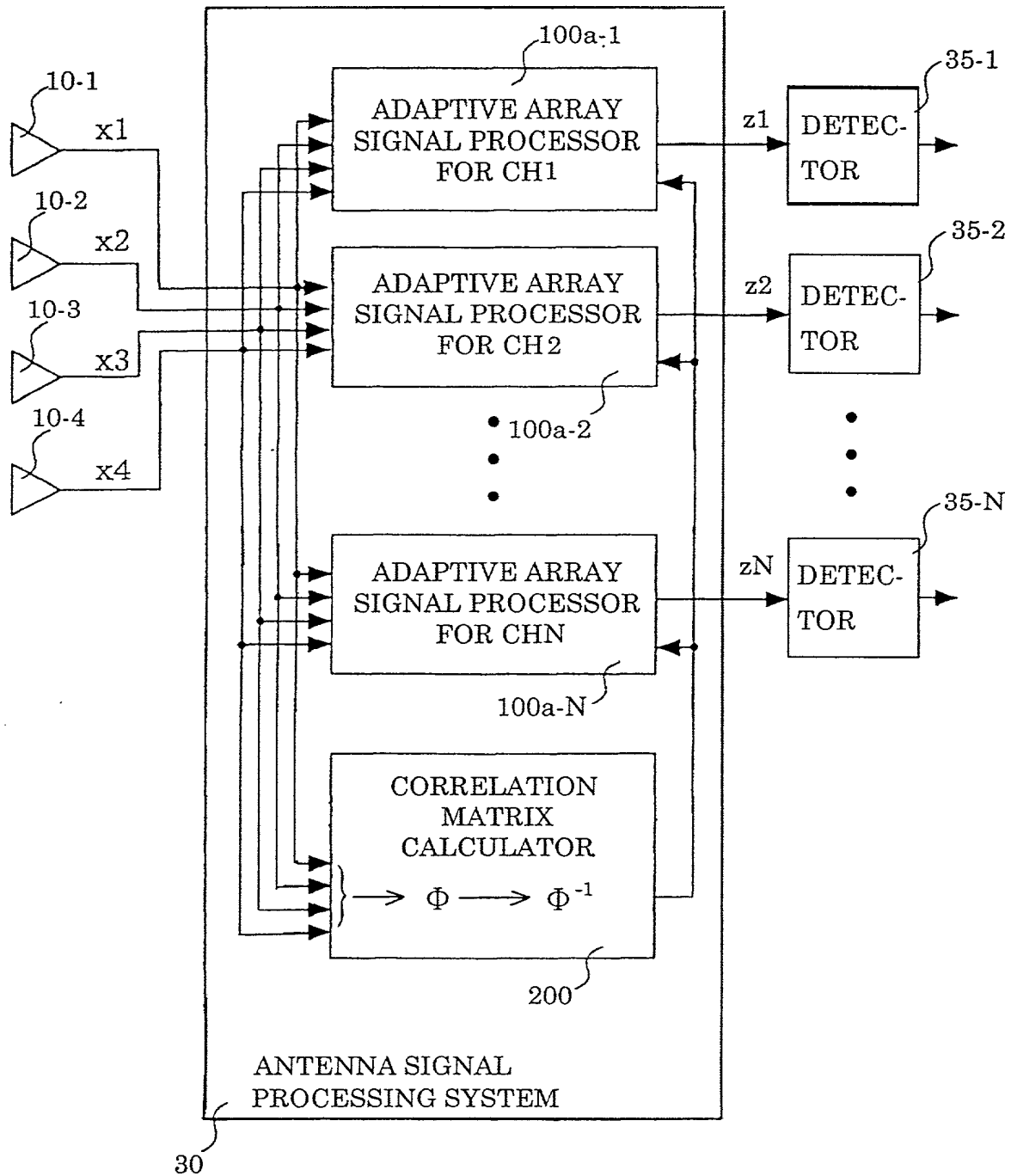
2

FIG. 4

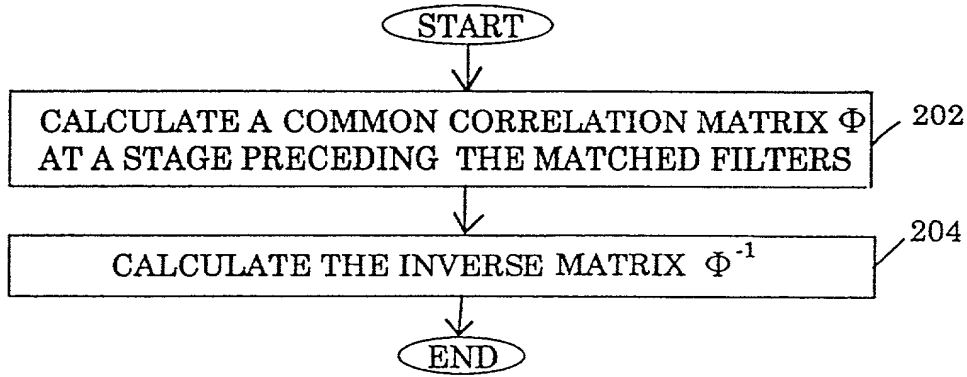


FIG. 5

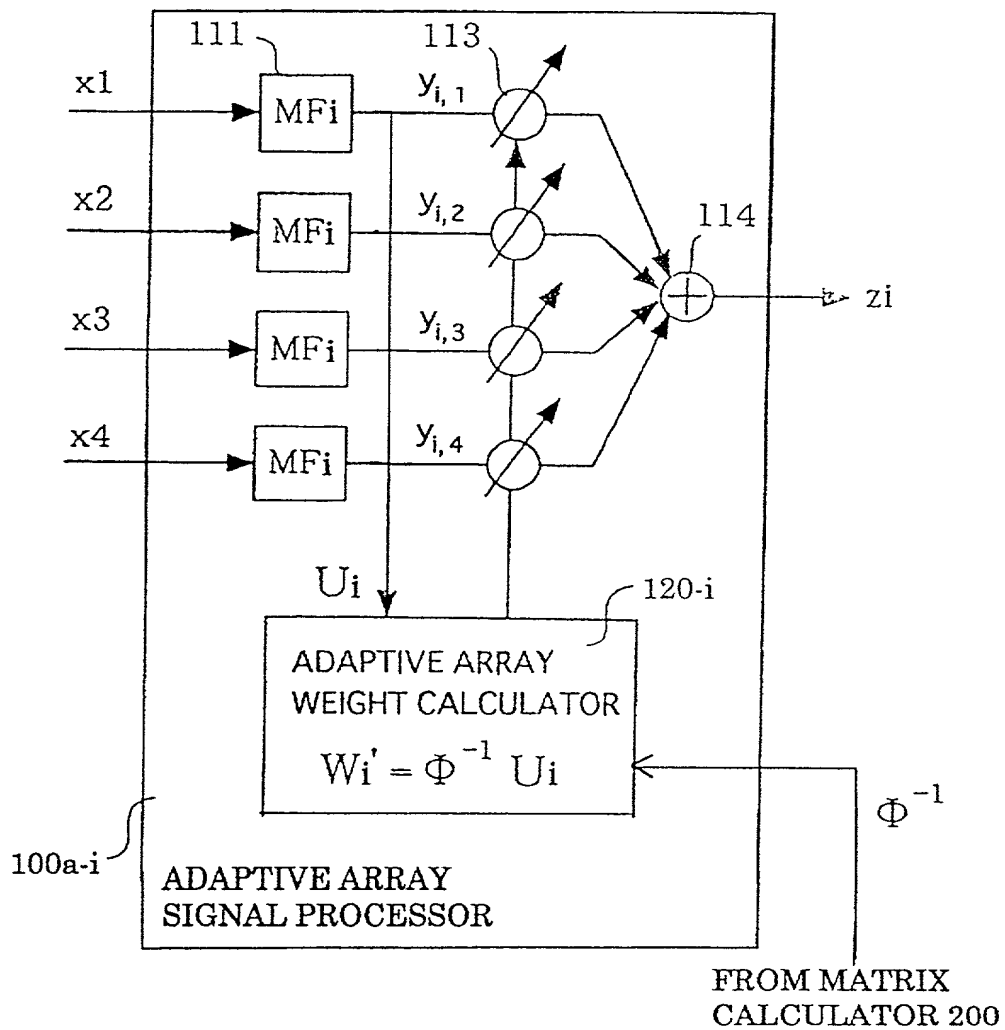


FIG. 6

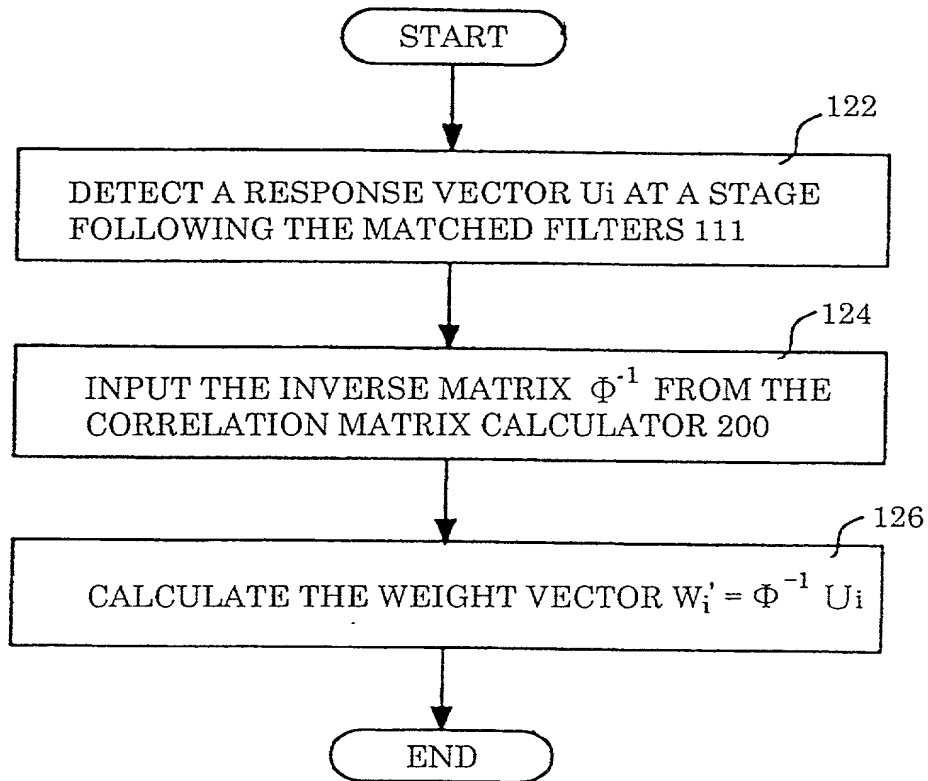
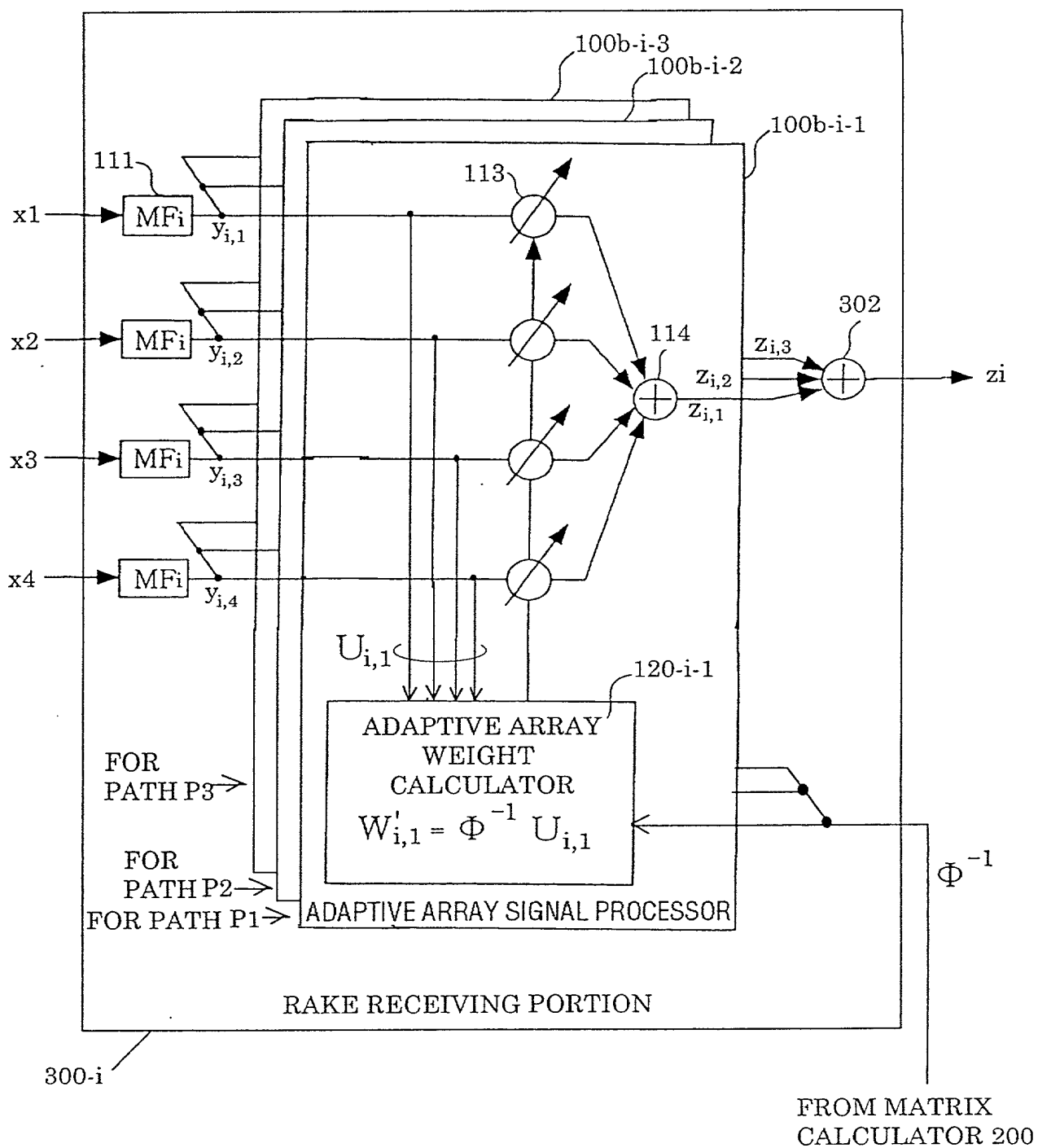


FIG. 7



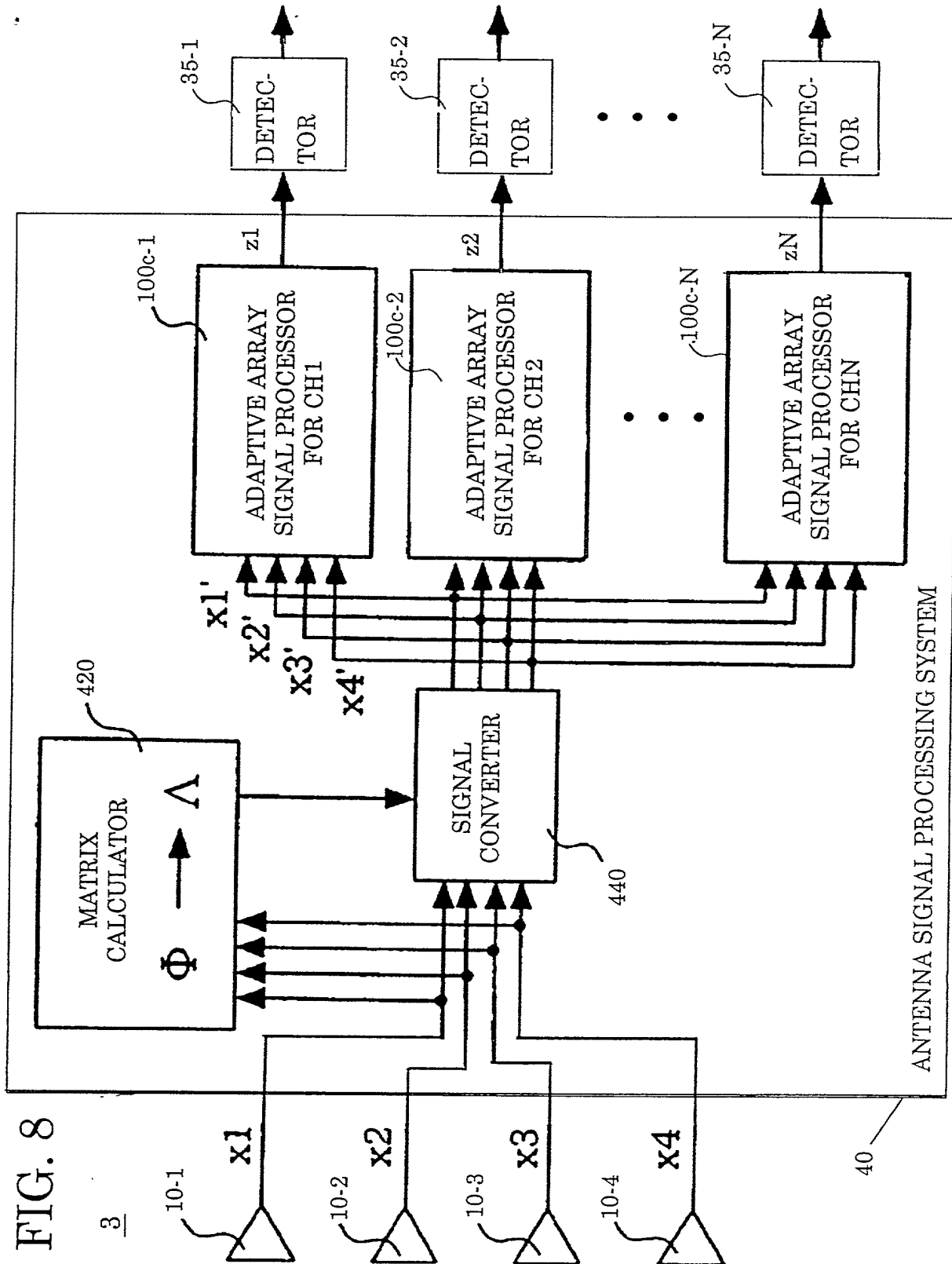


FIG. 8

31

10-1

18

10-2

2X

10-3

3X

10-4

4x

40

MATRIX CALCULATOR

①

1

+

SIGNAL CONVERTER

440

100c-1

AY

00c-2

AY 72

ALL
SOR

100c-N

AY

SOR

35-1

DETC-

35-2

DETEC-

35-N

DETEC

ANTENNA SIGNAL PROCESSING SYSTEM

FIG. 9

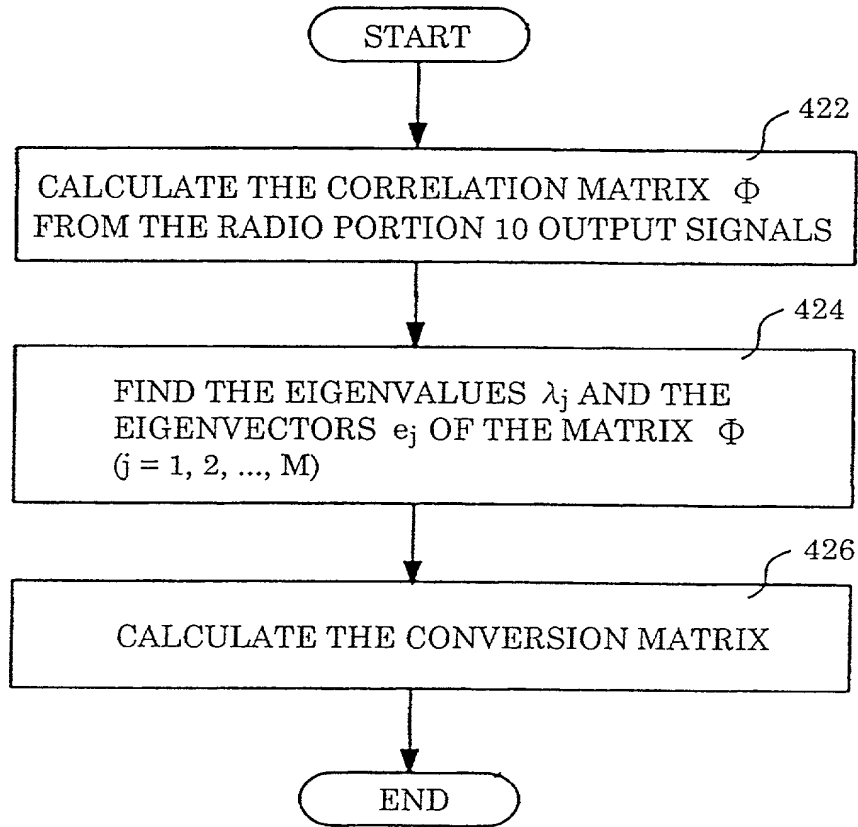


FIG. 10

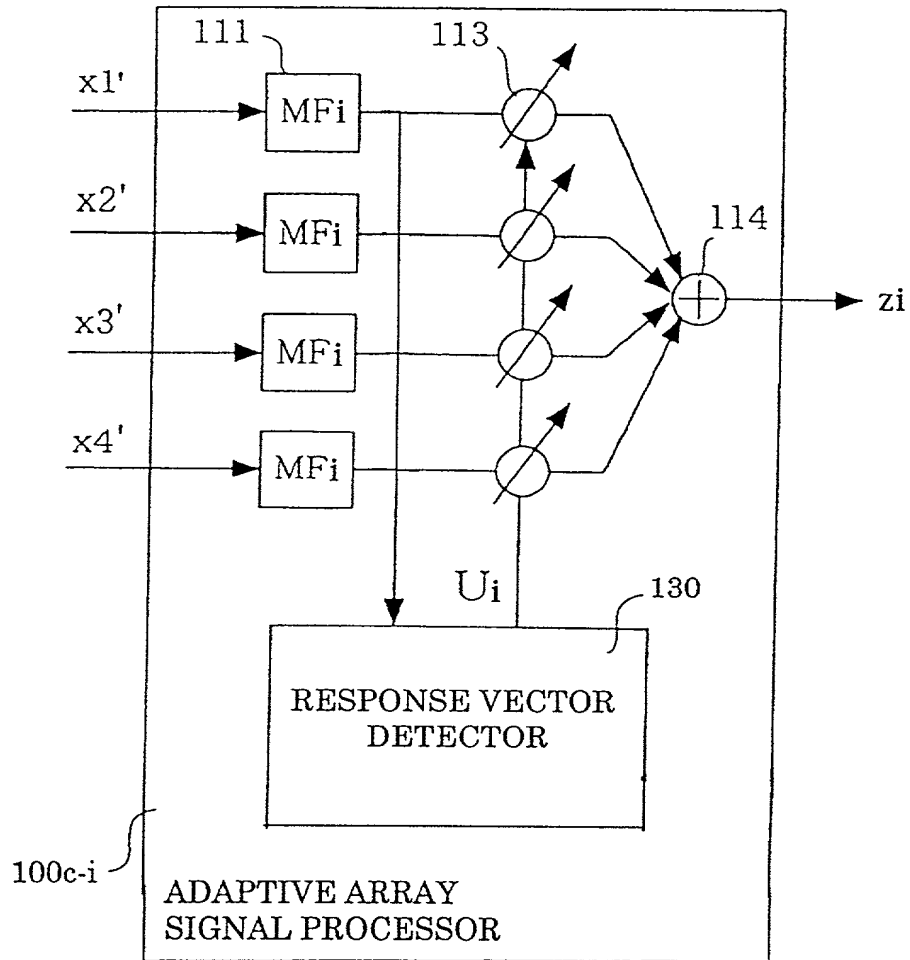


FIG. 11

